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Applicant: William R. Wheeler et al.

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Title

: UNIFIED DESIGN PARAMETER DEPENDENCY MANAGEMENT

AND APPARATUS

SEP 2 2 2003

Attention: Official Draftsman Commissioner for Patents

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Technology Center 2100

TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed four (4) sheets of formal drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

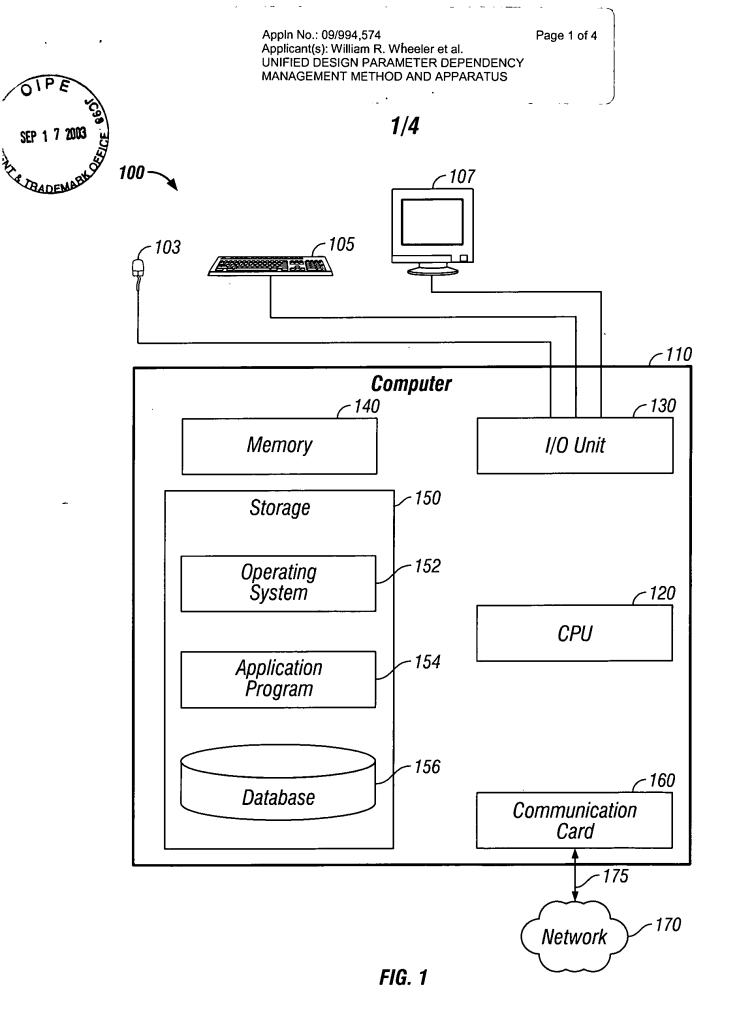
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MANAGEMENT METHOD AND APPARATUS



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200-

Upper Data Field		Lower Data Field		Status		Parity	
31	16	15	6	5	3	2	0
240		230		220-	,	210-	,

FIG. 2

300-

Data_Word_Range	31:0
Upper_Data_Range	31:16
Lower_Data_Range	15:6
Status_Range	5:3
Parity_Range	2:0
Data_Word_Width	32
Upper_Data_Width	16
Lower_Data_Width	10
Status_Width	3
Parity_Width	3

FIG. 3



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lower data field/LOWER DATA WIDTH-1:0J = data word/LOWER DATA RANGEJ; reg [PARITY_WIDTH-1:0] parity upper_data_field[UPPER_DATA_WIDTH-1:0] = data_word[UPPER_DATA_RANGE]; $status[STATUS_WIDTH-1:0] = data_word[STATUS_RANGE];$ $parity[PARITY_WIDTH-1:0] = data_word[PARITY_RANGE];$ reg [UPPER_DATA_WIDTH-1:0] upper_data_field; reg [LOWER_DATA_WIDTH-1:0] lower_data_field; reg [DATA_WORD_WIDTH-1:0] data_word; reg [STATUS_WIDTH-1:0] status;

FIG. 4A

 $upper_data_field[15:0] = data_word[31:16];$ $lower_data_field[9:0] = data_word[15:6];$ $status[2:0] = data_word[5:2];$ $parity[2:0] = data_word[2:0];$ reg [15:0] upper_data_field; reg [9:0] lower_data_field; reg [2:0] status; reg [31:0] data_word, reg [2:0] parity;

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Define a Signal Parameter Value

520

Maintain the Defined Signal Parameter Value in a Central Database

530

Use the Defined Signal Parameter that is Maintained in the Central Database in Computer Code for a Logic Design

FIG. 5